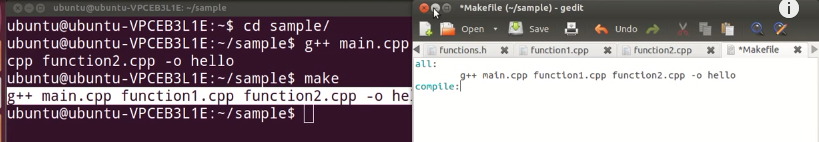
Makefile demo form programming knowledge

1.create an Empty Document called Makefile

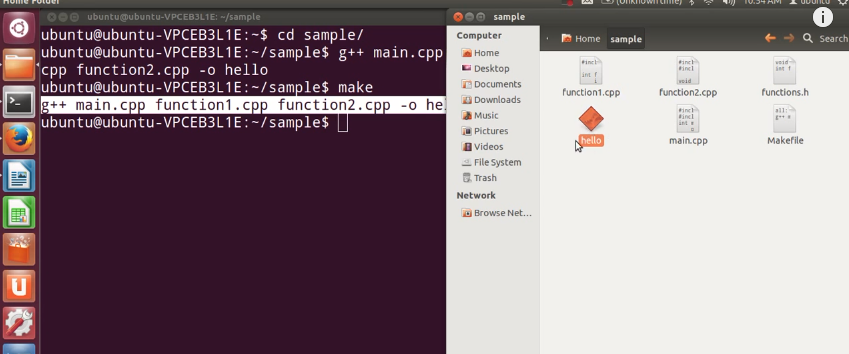
2.The target at written first in file is getting executing by default

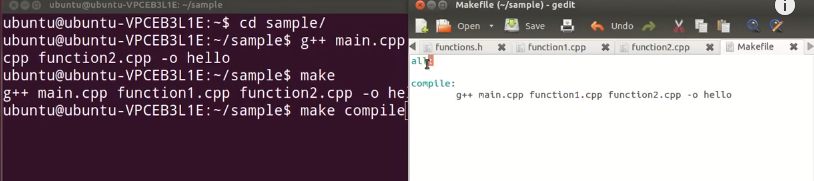
All:

[tab] gcc 1.cpp 2.cpp –o myprogram



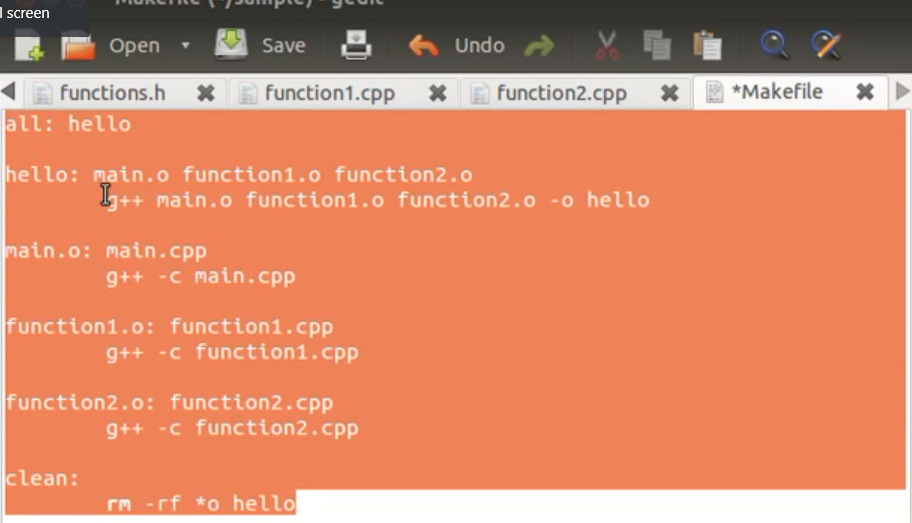
As you see compile is not running anymore





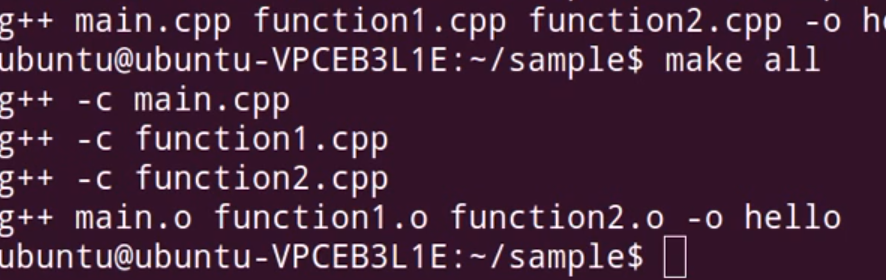
So for 2nd target you have to specify the target name to make that is compile

Comment is given through hash



Main.o(target):dependency(main.cpp).so this is basically file in present on disk.

Now g++ -c main.cpp is executed only if file is present



Sequence of compiling

